

FIG.1 PRIOR ART

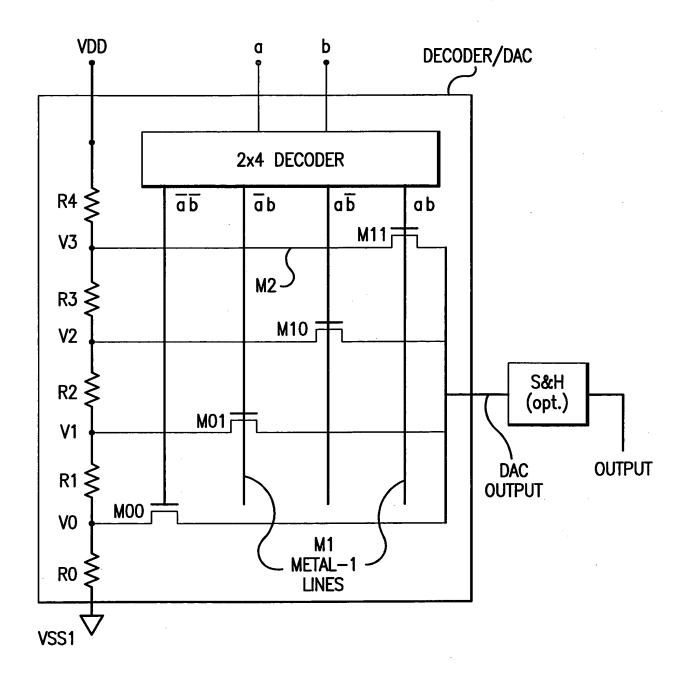


FIG.2 PRIOR ART

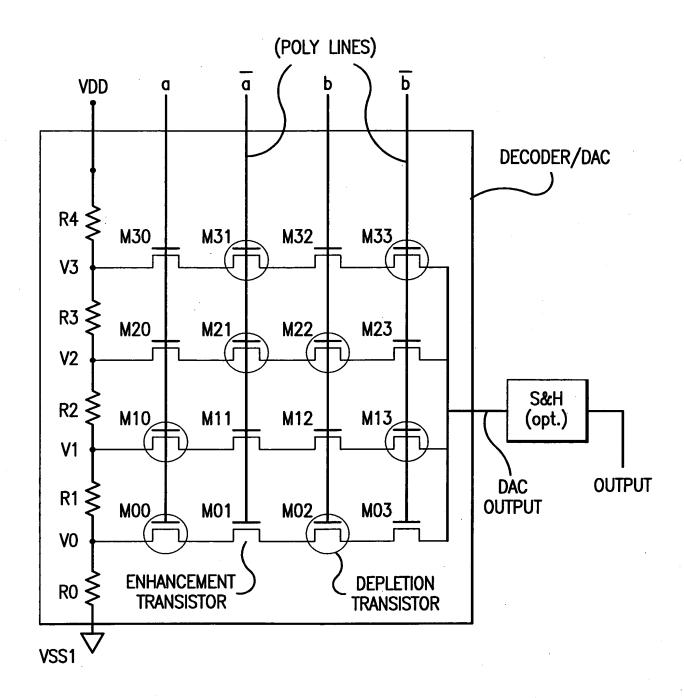


FIG.3
PRIOR ART

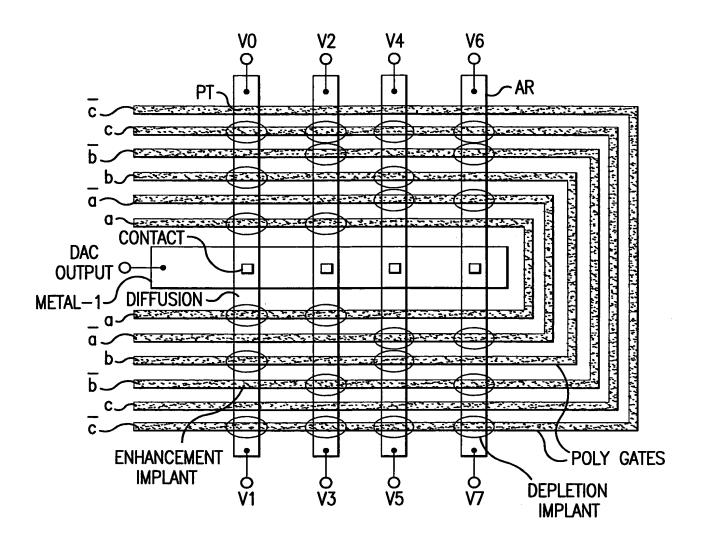


FIG.4 PRIOR ART

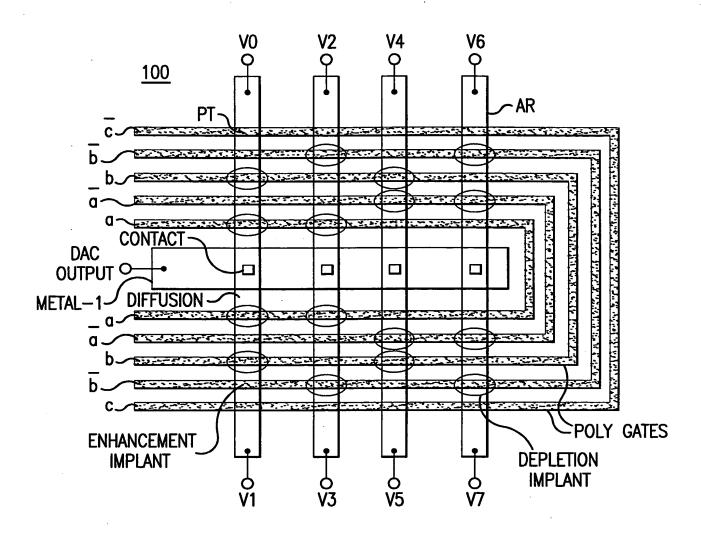
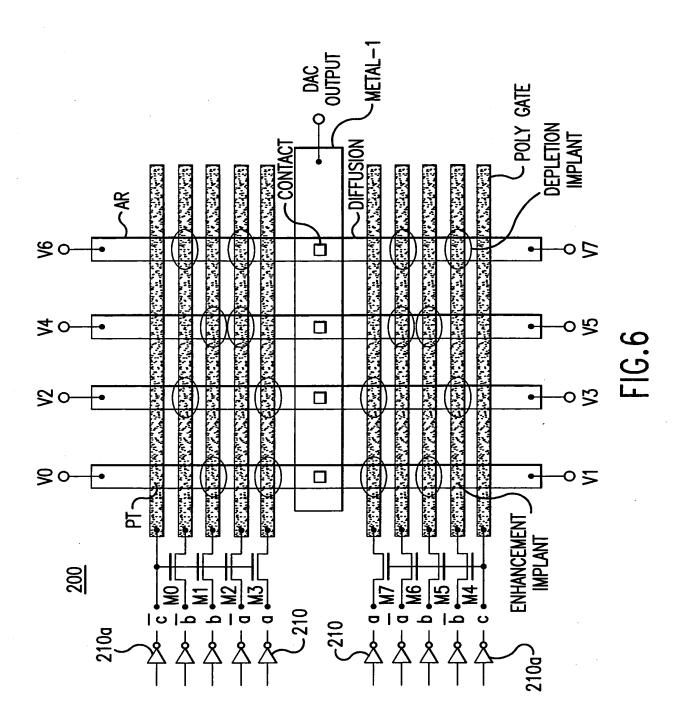
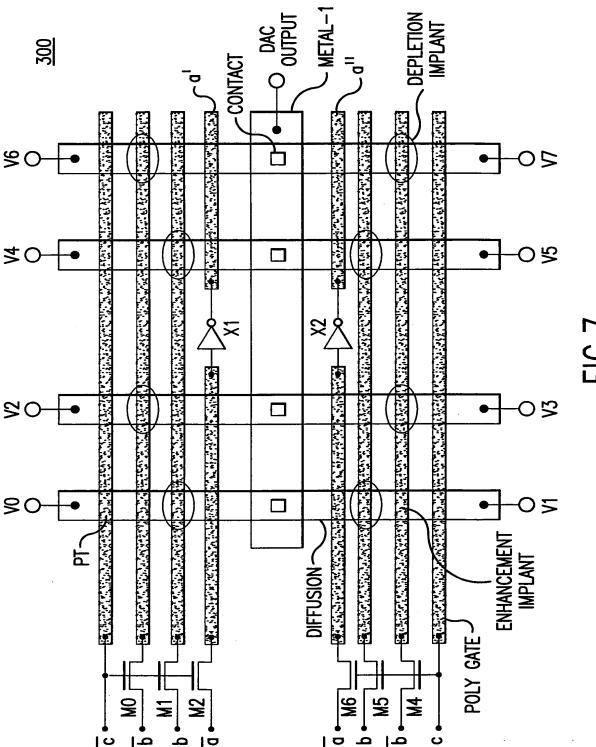
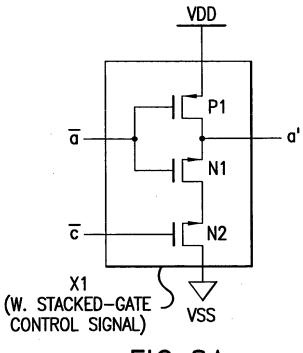


FIG.5





F1G.7



W. STACKED-GATE VSS

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FIG. 8A

FIG. 8B

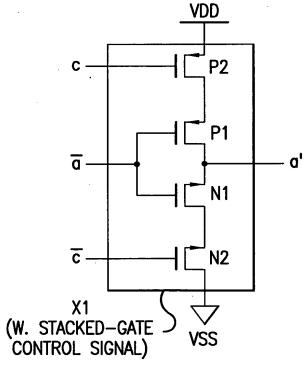


FIG. 9A

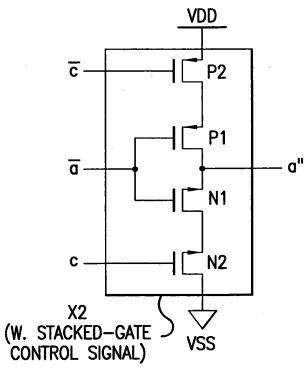
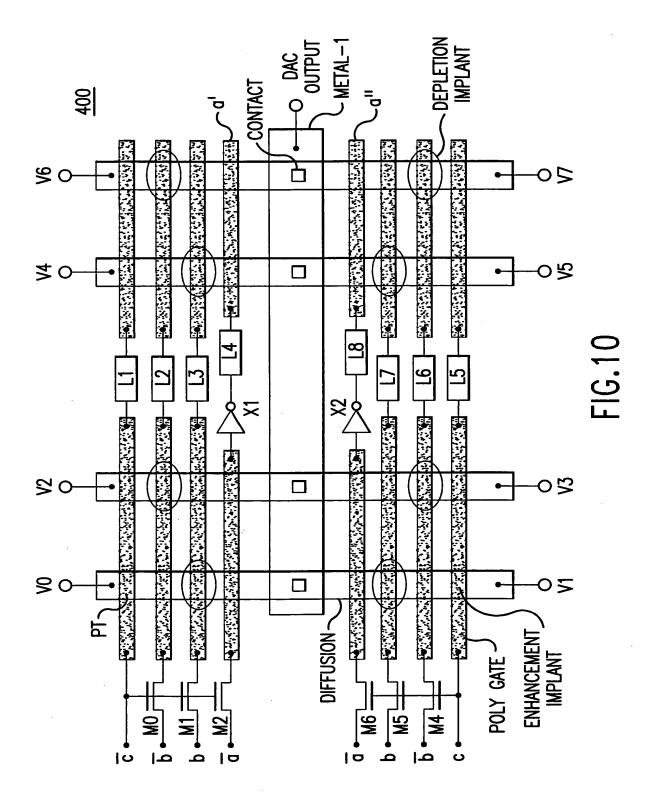


FIG.9B



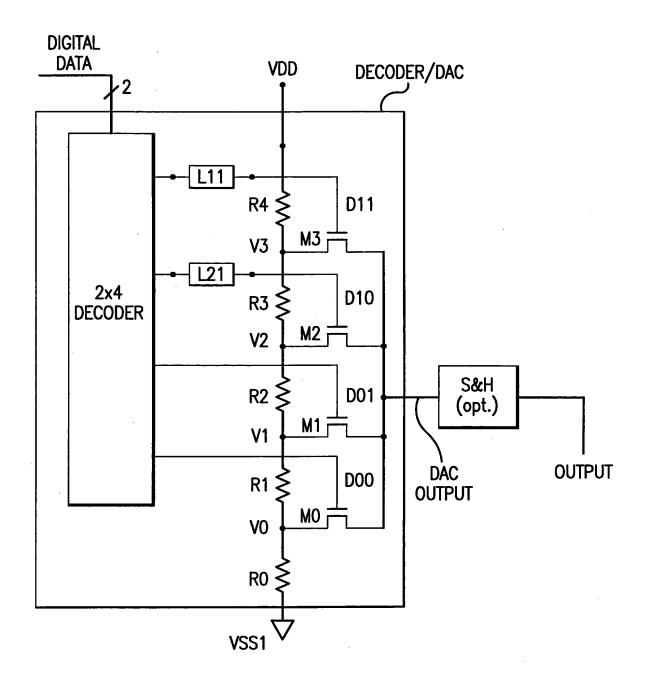


FIG. 11